

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	100628	driv\$\$ and switch and transistor	USPA T; EPO; JPO	2004/05/11 08:12
2	BRS	L2	25127	control and (vg or vgs or (gate adj voltage))	USPA T	2004/05/11 08:13
3	BRS	L3	233132	detect\$4 and (delay or (predeterm\$4 near time))	USPA T	2004/05/11 08:15
4	BRS	L4	2738	1 and 2 and 3	USPA T	2004/05/11 08:16
5	BRS	L5	105668	(abnormal\$3 or (over adj current) or (over adj voltage))	USPA T	2004/05/11 08:17
6	BRS	L6	495	4 and 5	USPA T	2004/05/11 09:24
7	BRS	L12	208	6 and (short near3 circuit)	USPA T	2004/05/11 09:24